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CMOS RF Power Amplifiers: Nonlinear, Linear, Linearized David Su Atheros Communications Sunnyvale, California. 2of41 IEEE Local Chapter Nov 2002 Outline 1. Introduction Nonlinear Power Amplification Linear Power Amplification Linearization using Envelope Elimination and Restoration Conclusion. 3of41 IEEE Local Chapter Nov 2002 RF Power Amplification Purpose: Delivers
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Linear CMOS RF Power Amplifiers for Wireless Applications

The RF power amplifier is a key component in a wireless transceiver and is considered by many as the design bottleneck in the transmitting chain. Linear CMOS RF Power Amplifiers for Wireless Applications addresses two fundamental aspects in RF power amplifier design for integration in CMOS technologies at 2.4, 3.7 and 5.2 GHz: efficiency enhancement and frequency agility.

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CMOS RF power amplifier design approaches for wireless

This dissertation focuses on the design of CMOS power amplifiers for modern wireless handsets, where stringent linearity requirements and high power efficiency are difficult to achieve simultaneously. CMOS technology has been an attractive technology for research in fully- integrated transceivers

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